

CLAIMS

What is being claimed is:

1. A semiconductor light emitting device comprising a light emitting layer disposed between an n-type region and a p-type region, wherein:
the light emitting layer is wurtzite;
a $\langle 0001 \rangle$ axis is substantially parallel to a top surface of the light emitting layer; and
the light emitting layer has a thickness greater than 25 Å.
2. The device of claim 1 wherein the light emitting layer has a thickness greater than 50 Å.
3. The device of claim 1 wherein the light emitting layer has a thickness greater than 90 Å.
4. The device of claim 1 wherein the light emitting layer has a thickness greater than 150 Å.
5. The structure of claim 1 wherein the light emitting layer comprises $\{11\bar{2}0\}$ InGaN.
6. The structure of claim 1 wherein the light emitting layer comprises $\{10\bar{1}0\}$ InGaN.
7. The structure of claim 1 wherein the light emitting layer comprises one of AlGaN and AlInGaN.
8. The device of claim 1 wherein a composition of indium in the light emitting layer is graded from a first indium composition in a first portion of the light emitting layer proximate the n-type region to a second indium composition in a second portion of the light emitting layer proximate the p-type region.
9. The device of claim 8 wherein the first composition is greater than the second composition.
10. The device of claim 8 wherein the first composition is less than the second composition.
11. The device of claim 8 wherein a composition of aluminum in the light emitting layer is graded from a first aluminum composition in a first portion of the light emitting layer proximate the n-type region to a second aluminum composition in a second portion of the light emitting layer proximate the p-type region.

12. The device of claim 1 wherein a composition of aluminum in the light emitting layer is graded from a first aluminum composition in a first portion of the light emitting layer proximate the n-type region to a second aluminum composition in a second portion of the light emitting layer proximate the p-type region.

13. The device of claim 12 wherein the first composition is greater than the second composition.

14. The device of claim 12 wherein the first composition is less than the second composition.

15. The device of claim 1 wherein the light emitting layer is a first quantum well, the device further comprising:

a second quantum well; and

a barrier layer disposed between the first and second quantum well;

wherein the first quantum well, second quantum well, and barrier layer form an active region.

16. The device of claim 15 wherein an indium composition in one of the first and second quantum wells is graded.

17. The device of claim 15 wherein the barrier layer has a graded composition.

18. The device of claim 15 wherein the active region is disposed between first and second cladding layers.

19. The device of claim 15 wherein the first and second cladding layers have a larger band gap than the first and second quantum wells.

20. The device of claim 15 wherein:

each of the first and second cladding layers is graded from a first band gap in a portion of the cladding layers adjacent to the active region to a second band gap in a portion of the cladding layers spaced apart from the active region; and

the second band gap is greater than the first band gap.

21. The device of claim 20 wherein a composition of indium in the first and second cladding layers is graded.

22. The device of claim 1 wherein the device operates at a current density greater than 10 A/cm^2 .

23. The device of claim 1 wherein the device operates at a current density greater than 100 A/cm^2 .